

NUMBER GS-12-1706	TYPE GENERAL PRODUCT SPECIFICATION	Amphenol ICC	
TITLE Minitek Pwr CEM-5 12VHPWR Wire to Board Connectors		PAGE 1 of 9	REVISION A
		AUTHORIZED BY MITHUN PAUL	DATE 23-06-2021
		CLASSIFICATION UNRESTRICTED	

1.0 Objective

This specification defines the performance, test, quality and reliability requirements of the Minitek Pwr CEM-5 12VHPWR Wire to Board Connectors product.

2.0 Scope

This specification is applicable to the termination characteristics of the Minitek Pwr CEM-5 family of products which provides high current rating and signal transmission.

3.0 Ratings

- 3.1 Operating Voltage Rating = 12V DC
- 3.2 Operating Power = 600 W @ 12VDC
- 3.3 Operating Current Rating
 - For Power Pin = 9.5A/pin (12 pins energized)
 - For Signal Pin = 1 A/pin
- 3.4 Applicable wire size

Termination	Wire Size	Max Outside insulation Diameter
Power	16 AWG	2.20 mm
Signal	28 AWG	1.27 mm

- 3.5 Operating Temperature Range = -40 ~ 105 (°C) ¹

Note 1: includes the terminal temperature rise when powered

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4.0 Applicable Documents

4.1 AFCI Specifications

4.1.1 Engineering drawings

Series Number	Description
10161719	Wire Connector, Receptacle HSG
10160920	Board Connector, Right Angle Header with PWR & Signal Terminals
10132447	Wire Connector, Receptacle Crimp PWR Terminal
10161952	Wire Connector, Receptacle Crimp Signal Terminal

4.1.2 Application specification: GS-20-0704

4.2 Industry or Trade Association standards

- PCI Express CEM Specification, Revision 5.0, Version 1.0

4.3 National or International Standards

4.3.1 Flammability: UL94V-0 or similar applicable specification (Under Testing)

4.3.2 EIA 364: Electrical Connector/Socket Test Procedures Including Environmental Classifications.

4.3.3 IEC 60512: Connectors for Electronic Equipment – Tests and Measurement

4.4 AFCI Laboratory Reports - Supporting Data

- ELX-05/21/021

4.5 Safety Agency Approvals

UL File: In progress

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5.0 Requirements

5.1 Qualification

Connectors furnished under this specification shall be capable of meeting the qualification test requirements specified herein.

5.2 Material

The material for each component shall be as specified herein or equivalent.

5.2.1 Header Housing - Thermoplastic Glass Fiber Filled, UL 94V-0

5.2.2 Receptacle Housing – Thermoplastic, UL 94V-0

5.2.3 Terminal Material – Copper Alloy

5.2.4 Wire – 16 AWG Insulated Stranded Tinned copper conductors for PWR Terminals

5.2.5 Wire – 28 AWG Insulated Stranded Tinned copper conductors for Signal Terminals

5.3 Finish

The finish for applicable components shall be as specified herein or equivalent.

5.3.1 Contact Area:-

- Tin Plating Overall (Refer to individual product drawing)
- Gold Flash Overall
- 15µ" Gold On Contact Area
- 30µ" Gold On Contact Area

5.3.2 Crimp Area:-

- Tin plating (Refer to individual product drawing)

5.4 Design and Construction

Connectors shall be of the design, construction, and physical dimensions specified on the applicable product drawing. There shall be no cracks, burrs, or other physical defects that may impair performance.

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6.0 Performance

6.1 Electrical Requirements

TEST ITEM		REQUIREMENT	PROCEDURE
1	Low Level Contact Resistance	5 milliohms MAXIMUM [initial] For Power Terminal	Mate connectors: apply a maximum voltage of 20 mV and a current of 100 mA. (Does not include wire resistance) (As per EIA 364-23)
		10 milliohms MAXIMUM [initial] For Signal Terminal	
2	Insulation Resistance	1000 Mega ohms MINIMUM	Unmate & unmount connectors: apply a voltage of 500 VDC between adjacent terminals and between terminals to ground. (As per EIA 364-21)
3	Dielectric Withstanding Voltage	No breakdown or flashover. Current leakage < 5mA	Unmate connectors: apply a voltage of two times the rated voltage plus 1000volts VAC for 1 minute between adjacent terminals and between terminals to ground (As per EIA-364-20)
4	Temperature Rise (Via Current Cycling)	Temperature rise: +30°C max.	Mate connector: measure the temperature rise at the rated current as specified in section 3.3

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6.2 Mechanical Requirements

	TEST ITEM	REQUIREMENT	PROCEDURE
5	Insertion and withdrawal force	8.0 N per circuit Max. Insertion force & 2.4 N per circuit Min. withdrawal force	Mate and unmate connector (male to female) at a rate of 25±6mm per minute (As per EIA 364-13)
6	Terminal Retention Force (in Housing)	15 N Min. retention force For Power Terminal	Axial pullout force on the terminal in the housing at a rate of 25 ± 6 mm (1 ± ¼ inch) per minute. (As per EIA 364-05)
		9 N Min. retention force For Signal Terminal	
7	Terminal Insertion Force (into Housing)	14.7 N Max. insertion force	Apply an axial insertion force on the terminal at a rate of 25 ± 6 mm (1 ± ¼ inch) per minute (As per EIA 364-05)
8	Durability	LLCR: Δ5 mΩ max. For Power Terminal	Mate connectors up to 30 cycles at a maximum rate of 10 cycles per minute (As per EIA-364-09)
		LLCR: Δ10 mΩ max. For Signal Terminal	
9	Wire Pullout Force (Axial) (Wire from Terminal)	Min. pullout force 16 awg: 68.6 N 28 awg: 8.9 N	Apply an axial pullout force on the wire at a rate of 25+/-6mm per minute. (As per IEC 60512)
10	Pin to Header Retention	13.7 N Min. pushout force For Power Terminal	Apply axial push force to pin at a rate of 25 ± 6 mm (1 ± ¼ inch) per minute. (As per EIA 364-29)
		9 N Min. pushout force For Signal Terminal	
11	Thumb Latch to Ramp Yield Strength	58.0 N MINIMUM Yield Strength	Full mate and then unmated the connector at a rate of 25+/-6mm per minute.

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6.3 Environmental Requirements

	TEST ITEM	REQUIREMENT	PROCEDURE
12	Solderability	Continuous solder coating with a min. 95% coverage	Solder pot temperature: 250 ~255 °C Soldering time: 2.5 seconds Flux: unactivated (As per IEC-60068-2-20)
13	Salt Spray	No evidence of damage.	Mate connectors Orientation: Horizontal with latch on top surface Duration: 48 hours exposure Atmosphere: Salt spray from a 5% solution Temperature: 35 ± 2°C As per EIA-364-26 (As per EIA 364-17)
		LLCR: Δ5 mΩ max. For Power Terminal	
14	Thermal Aging	LLCR: Δ10 mΩ max. For Signal Terminal	Mate connectors: expose to 240 hours at 105 ± 2°C (As per EIA 364-17)
		LLCR: Δ5 mΩ max. For Power Terminal	Mate connectors Expose gold plated terminals to: 1,000 hours at 125 ± 2 °C
15	Humidity (Steady State)	Dielectric Withstanding Voltage: No Breakdown at 500 VAC	Mate connectors: expose to a temperature of 40 ± 2°C with a relative humidity of 90~95% for 96 hours. Note: Remove surface moisture and air dry for 1 hour prior to measurements. EIA 364-31 method II (steady state)
		Insulation Resistance: 1000 MΩ Min.	
		LLCR: Δ5 mΩ max. For Power Terminal	
16	Resistance to Soldering Heat	LLCR: Δ10 mΩ max. For Signal Terminal	Dip connector terminal tails in solder: Solder Duration: 10 seconds Max. Solder temperature: 260°C Max. (As per EIA-364-56)
		Visual: No Damage to insulator material	
17	Cold Resistance	LLCR: Δ5 mΩ max. For Power Terminal	Mate connectors Duration:96 hrs Temperature: -40±3°C (Per EIA 364-59)
		LLCR: Δ10 mΩ max. For Signal Terminal	

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7.0 QUALITY ASSURANCE PROVISIONS

7.1 Equipment Calibration

All test equipment and inspection facilities used in the performance of any test shall be maintained in a calibration system in accordance with ANSI Z-540 and ISO 9000.

7.2 Inspection Conditions

Unless otherwise specified herein, all inspections shall be performed under the following ambient conditions:

- a. Temperature: 25 +/- 5 deg C
- b. Relative Humidity: 30% to 60%
- c. Barometric Pressure: Local ambient

7.3 Acceptance

9.4.1 Electrical and mechanical requirements placed on test samples as indicated in paragraphs 6.0 and 7.0 shall be established from test data using appropriate statistical techniques or shall otherwise be customer specified, and all samples tested in accordance with this product specification shall meet the stated requirements.

9.4.2 Failures attributed to equipment, test setup, or operator error shall not disqualify the product. If product failure occurs, corrective action shall be taken and samples resubmitted for qualification.

7.4 Qualification Testing

Qualification testing shall be performed on sample units produced with equipment and procedures normally used in production. The test sequences shall be as shown in the qualification test table. Data shall be provided with the samples noting production history: production lot codes for components and assemblies, components and assemblies produced to print revision, verification of plating composition and thickness, etc.

7.5 Re-Qualification Testing

If any of the following conditions occur, the responsible product engineer shall initiate requalification testing consisting of all applicable parts of the qualification test matrix.

- a. A significant design change is made to the existing product which impacts the product form, fit or function. Examples of significant changes shall include, but not be limited to, changes in the plating material composition or thickness, contact force, contact surface geometry, insulator design, contact base material, or contact lubrication requirements.
- b. A significant change is made to the manufacturing process which impacts the product form, fit or function.
- c. A significant event occurs during production or end use requiring corrective action to be taken relative to the product design or manufacturing process.

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7.6 Qualification Test Table

Test or Examination	Test Group										
	A	B	C	D	E	F	G	H	I	J	K
	Test Sequence										
Examination of Product	1,9	1,11	1,5	1,3	1	1	1	1	1,3	1,5	1,3
Low Level Contact Resistance	2,8	2,6, 10	2,4		2,4					2,4	
Insulation Resistance		3,8									
Dielectric Withstanding Voltage		4,9									
Temperature Rise (Via Current Cycling)			3								
Insertion Force	3,6										
Withdrawal Force	4,7										
Terminal Insertion Force in Housing for Cable assembly						2					
Terminal Retention Force in Housing for Cable assembly						3					
Wire Pullout Force (Axial) (Wire from Terminal)							2				
Thumb Latch to Ramp Yield Strength								2			
Durability	5										
Pin to header Retention											2
Solderability				2							
Salt Spray					3						
Thermal Aging		5									
Humidity (Steady State)		7									
Resistance to Soldering Heat									2		
Cold Resistance										3	
Sample Size per Test Group	5	5	5	5	5	5	5	5	5	5	5

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REVISION RECORD

Rev	Page	Description	EC#	Date
A	ALL	New Release		23-06-2021